



JPW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN THE MATTER OF:

Chung-Che TSAI

GROUP: 2826

SERIAL NO.: 10/649,884

EXAMINER: Alexander O. Williams

FILED: August 26, 2003

FOR: STACKED-CHIP SEMICONDUCTOR PACKAGE AND FABRICATION METHOD
THEREOF

R E S P O N S E

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

S I R:

This is in response to the outstanding Office Communication dated March 17, 2005, requesting Applicant to elect a single disclosed species under 35 USC 121 for prosecution on the merits. Any additional fee pertaining this application should be deducted from our Deposit Account. No. 01-1944.